

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,909,326 B2
DATED : June 21, 2005
INVENTOR(S) : Jesme, Ronald D.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

Item [56], **References Cited**, OTHER PUBLICATIONS,

"Solid-State Circuits,..." reference, delete "00118" and insert -- 0018 --.

Column 1,

Line 6, after "2001" insert -- , --.

Column 7,

Line 10, after "first" insert -- and --.

Line 13, delete "(MOSFBT'S)" insert -- (MOSEFET'S) --.

Line 24, after "apparatus" insert -- of --.

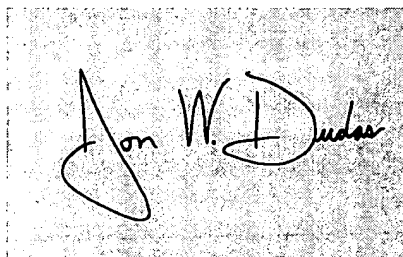
Column 8,

Lines 10 and 13, after "amplifier" insert -- of --.

Line 26, after "apparatus" insert -- of --.

Signed and Sealed this

Twenty-seventh Day of December, 2005

A handwritten signature in black ink, reading "Jon W. Dudas", is written over a rectangular area with a light gray, textured background.

JON W. DUDAS

Director of the United States Patent and Trademark Office